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13/101

Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

5c3
266

SA	O.I.P.E.	PATENT DATE
SCANNED	TR 2	Q.A. <u>dm</u>

APPLICATION NO. 09/904748	CONT/PRIOR	CLASS 707	SUBCLASS 710	ART UNIT 2171 2196	EXAMINER Shin
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Stephen Novak

A Method and apparatus for accessing memories having a time-variant response over a PCI bus by using two-stage DMA transfers

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PTO-2046
12/99

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Flgs. Drwg.	Print Fig. /	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of the patent have been disclaimed				ISSUE BATCH NUMBER	

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